

BOX PCT

IN THE UNITED STATES ELECTED OFFICE
OF THE UNITED STATES PATENT AND TRADEMARK OFFICE
UNDER THE PATENT COOPERATION TREATY - CHAPTER II

APPLICANT:	Utz Wever, et al.	ATTORNEY DOCKET NO.	P99,0886
SERIAL NO.		GROUP ART UNIT:	
FILING DATE:		EXAMINER:	
INTERNATIONAL APPLICATION NO.	PCT/DE97/02600		
INTERNATIONAL FILING DATE:	November 7, 1997		
INVENTION:	"COMPUTER-SUPPORTED METHOD FOR PARTITIONING AN ELECTRICAL CIRCUIT"		

PRELIMINARY AMENDMENT

To: Assistant Commissioner of Patents
Washington, D.C. 20231

S I R:

Amend the above-identified international application before entry into the national stage before the U.S. Patent and Trademark Office under 35 U.S.C. §371 as follows:

IN THE SPECIFICATION

At page 1, line 1, insert the title --TITLE--.

At page 1, line 4, insert the heading --BACKGROUND OF THE INVENTION--.

At page 1, line 4 after the title, insert the following paragraph:

--The present invention relates to computer-implemented systems for simulating electronic or electrical circuits.--

At page 1, line 18, change "It is" to --First, it is--.

At page 1, line 24, change "Further" to --Second--.

At page 2, line 1, after "for" insert --the--.

At page 2, line 2, change "known from document [1] as" to --the--.

At page 2, line 3, after "SPICE." insert --See I. Hoefer, et al., *SPICE Analyseprogramm für elektronische Schaltungen*, Springer, Berlin (1985), pp. 7-22.--